IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

APPLICANTS

Amit Merchant, et al.

SERIAL NO.

Continuation of 09/475,029

FILED

Parent Filed December 30, 1999

FOR

INTERFACE TO A MEMORY SYSTEM FOR A

PROCESSOR HAVING A REPLAY SYSTEM

GROUP ART UNIT:

TBD (Parent Case – 2154)

EXAMINER

TBD (Parent Case - Donaghue, L. D.)

ASSIGNEE

INTEL CORPORATION

PRELIMINARY AMENDMENT

Honorable Commissioner of Patents P.O. Box 1450 Alexandria, VA 22313-1450

Sir:

Prior to the examination of the above-identified application, please enter the following amendments to the specification and claims.